

The manufacturer may use the mark:



Revision 3.1 January 19, 2024 Surveillance Audit Due January 1, 2026



# Certificate / Certificat Zertifikat / **合格証**

EAS 1606010 C001

exida hereby confirms that the:

## CB/CBA/CBB/CBA300 Series Scotch Yoke Actuators

## Valve Automation Emerson Automation Solutions

Has been assessed per the relevant requirements of:

## IEC 61508 : 2010 Parts 1-7

and meets requirements providing a level of integrity to:

## Systematic Capability: SC 3 (SIL 3 Capable)

## Random Capability: Type A, Route 2<sub>H</sub> Device

**PFD**<sub>AVG</sub> and Architecture Constraints must be verified for each application

### Safety Function:

The actuator will move the actuator/valve combination to the designed safe position within the specified safety time.

### **Application Restrictions:**

The unit must be properly designed into a Safety Instrumented Function per the Safety Manual requirements.



Evaluating Assessor

**Certifying Assessor** 

Page 1 of 2

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## Systematic Capability: SC 3 (SIL 3 Capable)

### Random Capability: Type A, Route 2<sub>H</sub> Device

**PFD**<sub>avg</sub> and Architecture Constraints must be verified for each application

### Systematic Capability :

The product has met manufacturer design process requirements of Safety Integrity Level (SIL) 3. These are intended to achieve sufficient integrity against systematic errors of design by the manufacturer.

A Safety Instrumented Function (SIF) designed with this product must not be used at a SIL level higher than stated.

#### **Random Capability:**

The SIL limit imposed by the Architectural Constraints must be met for each element. This device meets *exida* criteria for Route  $2_{H}$ .

### IEC 61508 Failure Rates in FIT\*

Application	$\lambda_{SD}$	λ <sub>su</sub>	$\lambda_{DD}$	λ <sub>DU</sub>
Spring Return	0	168	0	354
Double Acting	0	0	0	500

\* FIT = 1 failure / 10<sup>9</sup> hours

### SIL Verification:

The Safety Integrity Level (SIL) of an entire Safety Instrumented Function (SIF) must be verified via a calculation of  $PFD_{avg}$  considering redundant architectures, proof test interval, proof test effectiveness, any automatic diagnostics, average repair time and the specific failure rates of all products included in the SIF. Each element must be checked to assure compliance with minimum hardware fault tolerance (HFT) requirements.

The following documents are a mandatory part of certification:

Assessment Report: EAS 16/06-010 R003 V4R1

Safety Manual: ES 47 Rev D or later



80 N Main St Sellersville, PA 18960